DRV8353R Register Settings for 1x PWM control with Hall effect Sensors

For Drive:

Driver Control Register: 0000 0001 0000 0100 0000

For Shutdown:

Driver Control Register: 0000001000001000100

SPI requirements:

Clock Polarity: 0

Clock Phase: 1

SPI Mode from Mbed: 1

16 bits per transaction

MSB first